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From: Richard J. Coldren Pages: 9
Date: August 21, 2003 Client/Matter No.: 013721-074563
I.D. No.: 8715
Messages:

Re: Attorney Docket No.: DB000575-010
Serial No.: ~~08/801,628~~

09/801 628

MT 8/19/03

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Applicant(s) Keeth et al. *transmittal letter x2
Serial No. 08/801,628 *PTO/SB/08A
Filing Date 08 March 2001 *PTO/SB/08B (2pp)
Amount of Check N/A *copies of 33 references
Atty's File No. DB000575-010
ELP/CFO/mk

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Date: 7 June 2001

PATENT

Attorney Docket No.: DB000575-010

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|---|---|-----------------------------------|
| Applicant(s): Keeth, et al. |) | Examiner: Not yet assigned |
| Serial No.: 08/801,628 |) | |
| Filed: 08 March 2001 |) | Art Unit: 2818 |
| Entitled: 256 MEG DYNAMIC RANDOM ACCESS MEMORY |) | |

TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In regards to the above-referenced patent application, transmitted herewith for filing are:

1. an Information Disclosure Statement By Applicant (Form PTO/SB/08A);
2. an Information Disclosure Statement By Applicant (Form PTO/SB/08B) (two pages); and
3. a copy of each of the thirty-three (33) references listed on Forms PTO/SB/08A and PTO/SB/08B.

Also enclosed is a return postcard. Please date stamp the postcard and return it to us to acknowledge receipt of this information disclosure statement.

The Commissioner is hereby authorized to charge any underpayment or credit any overpayment to our Deposit Account No. 20-0888. A duplicate copy of this Transmittal letter is enclosed.


Respectfully submitted,



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Dated: 17 July 2001

Attorney for Applicants

Please type a plus sign (+) inside this box → 

PTO/SB/08B (08-00)

Approved for use through 10/31/2002. OMB 0851-0031
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|---|---|--------------------------|------------------|
| Substitute for form 1449B/PTO | | Complete if Known | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | Application Number | 08/801,628 |
| | | Filing Date | 08 March 2001 |
| | | First Named Inventor | Keeth et al. |
| | | Group Art Unit | 2818 |
| | | Examiner Name | Not yet assigned |
| | | Attorney Docket Number | DB000575-010 |
| Sheet | 2 | of | 3 |

| OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS | | | |
|---|-----------------------|---|----------------|
| Examiner Initials ¹ | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | SUGIBAYASHI ET AL., "A 30-ns 256-Mb DRAM with a Multidivided Array Structure", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993) | |
| | | TAGUCHI ET AL., "A 40-ns 64-Mb DRAM with 64-b Parallel Data Bus Architecture", IEEE Journal of Solid-State Circuits, Vol. 26 (No. 11), (November 1991) | |
| | | KITSUKAWA ET AL., "256-Mb DRAM Circuit Technologies for File Applications", IEEE Journal of Solid-State Circuits, Vol. 28 (No. 11), (November 1993) | |
| | | JEDEC SOLID STATE PRODUCTS ENGINEERING COUNCIL, "Committee Letter Ballot", JC-42.3-95-73, Item #633.13, Arlington, VA (April 1995) | |
| | | YOO ET AL., "SP 23.6: A 32-Bank 1Gb DRAM with 1 GB/s Bandwidth", ISSCC96/Session 23/DRAM/PAPER SP 23.6 | |
| | | NITTA ET AL., "SP 23.6: A 1.6GB/s Data-Rate 1Gb Synchronous DRAM with Hierarchical Square-Shaped Memory Block and Distributed Bank Architecture", ISSCC96/Session 23/DRAM PAPER SP 23.5 | |
| | | U.S. Patent Application S.N. 08/521,563, entitled IMPROVED VOLTAGE REGULATOR CIRCUIT, filed 8/30/95 | |
| | | U.S. Patent Application S.N. 08/668,347, entitled DIFFERENTIAL VOLTAGE REGULATOR, filed 6/26/96 | |
| | | U.S. Patent Application S.N. 08/460,234, entitled SINGLE DEPOSITION LAYER METAL DYNAMIC RANDOM ACCESS MEMORY, filed 8/17/95 | |
| | | U.S. Patent Application S.N. 08/420,943, entitled DYNAMIC RANDOM-ACCESS MEMORY, filed 6/4/95 | |
| | | U.S. Patent Application S.N. 08/194,184, entitled INTEGRATED CIRCUIT POWER SUPPLY HAVING PIECEWISE LINEARITY, filed 2/8/94 | |

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|--------------------|--|-----------------|--|
| Examiner Signature | | Date Considered | |
|--------------------|--|-----------------|--|

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¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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